AMENDMENTS TO THE CLAIMS

This listing of claims will replace all prior versions and listings of claims in the application:

LISTING OF CLAIMS:

Claims 1-55 (canceled)

56. (original): A method of fabricating thin film transistors, comprising:

forming at least one conductive film on a substrate;

immersing said substrate in a dopant; and

delivering a laser beam to a plurality of independently selectable locations on said substrate to induce a reaction between said dopant and said conductive film at said independently selectable locations.

57. (original): The method claimed in claim 56, wherein said conductive film comprises a semiconductor film..

58. (original): The method claimed in claim 56, wherein said conductive film comprises silicon.

59. (original): The method claimed in claim 58, wherein said conductive film is deposited on an insulating substrate.

60. (original): The method claimed in claim 59 and wherein said insulating substrate comprises glass.

61. (original): The method claimed in claim 56, wherein said locations correspond to locations whereat transistors are to be formed in said conductive film.

62. (original): The method claimed in claim 56, wherein said immersing said substrate comprises showering ionized molecules onto said substrate.

63. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises splitting a first laser beam into a plurality of sub-beams, and delivering said sub-beams to mutually independently selectable locations.

64. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises scanning a pulsed laser beam across a surface, and synchronizing said scanning with pulses defining said pulsed laser beam to deliver at least one pulse to a selectable location.

65. (original): The method claimed in claim 64, wherein said delivering a laser beam further comprises modulating said laser beam to deliver said pulses exclusively to said selected locations.

66. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises delivering said laser beam to said selectable locations that are at least partially isolated from other said selectable locations.

67. (original): The method claimed in claim 56, wherein said delivering comprises delivering said laser beam to said selectable locations that vary from other said selectable locations in size.

68. (original): The method claimed in claim 56, wherein said delivering comprises delivering said laser beam to said selectable locations that vary from other said selectable locations in spacing.

69. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises modulating an energy characteristic of said laser beam.

70. (original): The method claimed in claim 69, wherein said delivering a laser beam comprises delivering said laser beam with a first modulated energy characteristic to a first independently selectable location, and then delivering said laser beam with a second modulated energy characteristic to a second independently selectable location.

71. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises delivering a pulsed laser beam having a pulse repetition rate of greater than 5 KHz.

72. (original): The method claimed in claim 56, further comprising individually heating each of said selectable locations to at least partially melt said conductive film thereat.

73. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises selecting locations during operation of a laser supplying said laser beam.

74. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises selecting locations during performing a reaction induction operation on said conductive film.

75. (original): The method claimed in claim 73, wherein said selecting comprises steering said laser beam to said selectable locations.

76. (original): The method claimed in claim 73, wherein said selecting comprises scanning said laser beam and modulating the beam during scanning.

77. (original): The method claimed in claim 56, wherein said delivering a laser beam comprises delivering said laser beam to said independently selectable locations without an intervening photo mask.

78. (original): The method claimed in claim 77, wherein said delivering a laser beam comprises selecting said independently selectable locations during operation of a laser supplying said laser beam.

79. (original): Thin film transistors on a substrate produced according to the method of claim 56.

Claims 80-110 (canceled)

111. (original): A method of producing thin film transistors on a substrate, comprising:

generating a laser beam;

splitting said laser beam into a plurality of selectably positionable sub-beams; and

directing each of said sub-beams to selectable areas on said substrate where said transistors are to be formed in the presence of a doping gas to induce a reaction between said substrate and said doping gas at said selectable areas.

112. (original): The method according to claim 111, wherein each of said selectable areas is at least partially isolated from another one of said selectable areas.

113. (original): The method according to claim 111, wherein at least one of said selectable areas vary from each other in size and spacing.

114. (original): The method according to claim 111, wherein said laser beam is a pulsed laser beam having a pulse repetition rate of greater than 5 KHz.

115. (original): The method according to claim 111, wherein said sub-beams are independently guided so as to interact with said doping gas and with said substrate surface, at each of said selectable areas.

116. (original): The method according to claim 111, further comprising individually heating each of said selectable areas thereby forming bases of said transistors.

117. (original): A thin film transistor formation system comprising:

a laser generating at least one laser beam;

a laser beam director directing said laser beams to selectable locations on a substrate in contact with a reactant,

wherein said laser beams are independently guided.

118. (original): The thin film transistor formation system according to claim 117, further comprising beam shaping lens to vary shape of said beams.

119. (original): The thin film transistor formation system according to claim 117, wherein said laser has a total average power of less than 50W.

120. (original): The thin film transistor formation system according to claim 117 further comprising a beam splitter splitting said laser beam into a plurality of sub-beams and a reflector configured to independently guide said sub-beams to locations on said substrate, wherein said reflector comprises a plurality of micromirrors directionally controlling each of said sub-beams.

121. (original): The thin film transistor formation system according to claim 120, wherein said sub-beams are focused by at least one focusing lens.

122. (original): The thin film transistor formation system according to claim 117, further comprising:

a processing chamber where said substrate is to be treated with said at least one laser beam;

a gas supply unit configured to supply a gas containing said reactant to said processing chamber, said substrate to be immersed in said gas during treatment with said at least one laser beam; and

a gas purging unit purging gas from said processing chamber.

123. (original): The thin film transistor formation system according to claim 122, wherein said gas reacts with said substrate when subject to said at least one laser beam at said selectable areas.

124. (original): The thin film transistor formation system according to claim 117, further comprising a controller controlling at least one of a fluence property of said at least one laser beam, a pulse rate of said at least one laser beam and a quantity of pulses impinging said substrate.

125. (original): The thin film transistor formation system according to claim 117, wherein said laser is a non-excimer pulsed laser.

126. (original): A method of manufacturing an array of thin film transistors, comprising:

depositing amorphous silicon on a substrate;

crystallizing said amorphous silicon by applying laser energy to said amorphous silicon at a plurality of selectable locations;

applying P type doping to portions of crystallized silicon by delivering laser energy to a plurality of selectable locations in presence of a P type doping agent.

127. (original): The method claimed in claim 126, wherein said applying P type doping comprises forming PMOS channels.

128. (original): The method claimed in claim 126, further comprising:

applying N type doping to portions of crystallized silicon by delivering laser energy to said plurality of selectable locations in presence of an N type doping agent.

129. (original): The method claimed in claim 128, wherein said applying N type doping comprises forming NMOS channels.

130. (original): The method claimed in claim 127, further comprising:

applying N type doping to portions of crystallized silicon by delivering laser energy to said plurality of selectable locations in presence of an N type doping agent.

131. (original): The method claimed in claim 130, wherein said applying N type doping comprises forming NMOS channels.

132. (original): The method claimed in claim 131, further comprising forming dielectric gates.

133. (original): The method claimed in claim 131, further comprising:

laser treating crystallized silicon at said selectable locations to hydrogenate said selectable locations.

134. (original): The method claimed in claim 132, further comprising:

laser treating said selectable locations in said crystallized silicon in the presence of said P doping agent to form PMOS type source/drains.

135. (original): The method claimed in claim 134, further comprising:

laser treating selected areas in said crystallized silicon in the presence of said N doping agent to form NMOS type source/drains.

136. (original): The method claimed in claim 126, wherein at least one of dehydrogenating, crystallizing said silicon, doping and hydrogenating is performed without masking.

137. (original): Thin film transistors on a substrate produced according to the method of claim 126.